(Department of Electronics & Communication Engineering)

1.Range for total credits in III, IV, V and VI semester is 22-28

2. A course on Management (PC), will be run in VII semester for four branches and the rest will have it in VIII semester

3. Course credit have been assigined as per R & R 8.7 of UG Manual.

4. Advance elective courses which can also be M.Tech courses or advance courses designed and offered to the UG students by the department have been identified.

| Categories: | PC: Programme Core PE: Programme Elective AEC: Advanced Elective | | | | | | | |
|--------------|--|---------|---|----|--------|----|-------|--|
| Semester III | SUM should be in the range 22-28 | | | | | | | |
| S.No. | Semester Course Code Course Name Category Type C | | | | | | L-T-P | |
| 1 | III | ECT 201 | Electronic Devices & Circuits | PC | Theory | 3 | 3-0-0 | |
| 2 | III | ECT 202 | Switching Theory & Finite Automata | PC | Theory | 3 | 3-0-0 | |
| 3 | III | ECT 203 | Network Theory | PC | Theory | 3 | 3-0-0 | |
| 4 | III | ECT 204 | Probabilistic Methods in Signals & System | PC | Theory | 3 | 3-0-0 | |
| 5 | III | ECT 205 | Graph Theory | PC | Theory | 2 | 2-0-0 | |
| 6 | III | ECT 206 | Data Structures & Algorithms | PC | Theory | 3 | 3-0-0 | |
| 1 | III | ECP 201 | Electronic Devices & Circuits Lab | PC | Lab | 2 | 0-0-3 | |
| 2 | III | ECP 202 | Switching Theory & Finite Automata Lab | PC | Lab | 2 | 0-0-3 | |
| 3 | III | ECP 204 | Data Structures & Algorithms Lab | PC | Lab | 2 | 0-0-3 | |
| 4 | III | ECP 206 | Probabilistic Methods in Signals & System | PC | Lab | 2 | 0-0-3 | |
| | - | - | • | • | - | 25 | | |

Semester IV

| Semester IV | emester IV SUM should be in the range 22-28 | | | | | | | | |
|-------------|---|-------------|--------------------------------|----------|--------|--------|-------|--|--|
| S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-P | | |
| 1 | IV | ECT 211 | Applied Electronics | PC | Theory | 3 | 3-0-0 | | |
| 2 | IV | ECT 212 | Analog Communication | PC | Theory | 3 | 3-0-0 | | |
| 3 | IV | ECT 213 | Microprocessors | PC | Theory | 3 | 3-0-0 | | |
| 4 | IV | ECT 214 | Electromagnetic Field Theory | PC | Theory | 3 | 3-0-0 | | |
| 5 | IV | ECT 215 | OPERATING SYSTEMS | PC | Theory | 3 | 3-0-0 | | |
| 6 | IV | ECT 216 | Measurements & Instrumentation | PC | Theory | 3 | 3-0-0 | | |

| 1 | IV | ECP 211 | Applied Electronics Lab | PC | Lab | 2 | 0-0-3 |
|---|----|---------|--------------------------|----|-----|---|-------|
| 2 | IV | ECP 212 | Analog Communication lab | PC | Lab | 2 | 0-0-3 |
| 3 | IV | ECP 213 | Microprocessors lab | PC | Lab | 2 | 0-0-3 |
| 4 | IV | ECP 215 | Operating Systems Lab | PC | Lab | 2 | 0-0-3 |

OE: Open Elective

(Department of Electronics & Communication Engineering)

| Semester V | | | | SUM should be | e in the ran | ge 22-28 | |
|------------|----------|-------------|--------------------------------|---------------|--------------|----------|-------|
| S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-P |
| 1 | V | ECT 301 | Microwave Engineering | PC | Theory | 3 | 3-0-0 |
| 2 | V | ECT 302 | Digital Signal Processing | PC | Theory | 4 | 3-0-2 |
| 3 | V | ECT 303 | Digital Communication Syustems | PC | Theory | 3 | 3-0-0 |
| 4 | V | ECT 304 | Digital CMOS IC | PC | Theory | 4 | 3-0-2 |
| 5 | V | ECT 305 | Optical Communication Systems | PC | Theory | 4 | 3-0-2 |
| 6 | V | ECT 306 | VLSI Testing & Testability | PC | Theory | 3 | 3-0-0 |

| 1 | V | ECP 301 | Microwave Engineering Lab | PC | Lab | 2 | 0-0-3 |
|---|---|---------|-----------------------------------|----|-----|----|-------|
| 2 | V | ECP 303 | Digital Communication Systems Lab | PC | Lab | 2 | 0-0-3 |
| - | | | | | | 25 | |

Semester VI

SUM should be in the range 22-28

| S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-P | | |
|-------|----------|-------------|---------------------------------|----------|--------|--------|-------|--|--|
| 1 | VI | ECT 311 | Antenna & Wave Propagation | PC | Theory | 4 | 3-0-2 | | |
| 2 | VI | ECT 312 | Computer Architecture | PC | Theory | 3 | 3-0-0 | | |
| 3 | VI | ECT 313 | Wireless & Mobile Communication | PC | Theory | 3 | 3-0-0 | | |
| 4 | VI | ECT 314 | Control System Engineering | PC | Theory | 3 | 3-0-0 | | |
| 5 | VI | ECT 315 | Embedded Systems | PC | Theory | 3 | 3-0-0 | | |
| 6 | VI | ECT 316 | Analog CMOS IC | PC | Theory | 3 | 3-0-0 | | |

| 1 | VI | ECP 316 | Analog CMOS IC lab | PC | Lab | 2 | 0-0-3 |
|---|----|---------|----------------------------|----|-----|----|-------|
| 2 | VI | ECP 317 | Embeded Systems Design Lab | PC | Lab | 2 | 0-0-3 |
| 3 | VI | ECS 318 | SEMINAR | PC | Lab | 2 | 0-0-3 |
| | | | | | | 25 | |

(Department of Electronics & Communication Engineering)

| nester VII | I | | | SUM should be | e in the ran | ge 22-28 | |
|------------|----------|-------------|---------------------|---------------|--------------|----------|----------|
| S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-I |
| 1 | VII | | Management* | PC | Theory | 3 | 3-0- |
| 2 | VII | | FROM LIST* | OE | Theory | 3 | 3-0- |
| 3 | VII | | FROM LIST* | OE | Theory | 3 | 3-0- |
| 4 | VII | | FROM AEC LIST* | PE | Theory | 3 | 3-0- |
| 5 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0- |
| 6 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0- |
| 7 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0- |
| 1 | VII | ECD 481 | Training Seminar | PC | Lab | 2 | 0-0-3 |
| 2 | VII | ECD 483 | System Design Lab-I | PC | Lab | 2 | 0-0-3 |
| 3 | VII | ECD 498 | Major Project A | PC | Project | 4 | 0-0- |
| | | | | | | | |
| | • | • | • | | • | 22 | <u>.</u> |

23

Semester VIII

SUM should be in the range 22-28

| S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-P |
|-------|----------|-------------|----------------|----------|--------|--------|-------|
| 1 | VIII | | Management* | PC | Theory | 3 | 3-0-0 |
| 2 | VIII | | FROM LIST* | OE | Theory | 3 | 3-0-0 |
| 3 | VIII | | FROM LIST* | OE | Theory | 3 | 3-0-0 |
| 4 | VIII | | FROM AEC LIST* | AEC | Theory | 3 | 3-0-0 |
| 5 | VIII | | FROM AEC LIST | AEC | Theory | 3 | 3-0-0 |
| 6 | VIII | | FROM AEC LIST | AEC | Theory | 3 | 3-0-0 |
| 7 | VIII | | FROM AEC LIST | | | 3 | 3-0-0 |

| 1 | VIII | ECD 482 | System Design Lab-II | PC | Lab | 2 | 0-0-3 | |
|---|------|---------|----------------------|----|---------|----|--------|------|
| 2 | VIII | ECD 499 | Major Project B | PC | Project | 8 | 0-0-16 | |
| | | | | | | 25 | | Tota |

149

*Indicates that the courses individually, may be opted either in 7th Semester OR 8th Semester

(Department of Electronics & Communication Engineering)

| | T OF PE/AEC: | : (3 credits each) | LIST OF PE/AE | C: (3 credits each) |
|--|---------------|---|---------------|---|
| ECT404/ECT 670 SATELLITE COMMUNICATION & RADAR ENGINEERING ECT 40/ECT 670 DESIGN OF MIC AND MMIC'S ECT 40/ECT 670 CAD ALGORITHMS FOR VLSI PHYSICAL DESIGN ECT 678 DESIGN OF MIC AND MMIC'S ECT 40/ECT 670 CAD ALGORITHMS FOR SYNTHESIS OF DIGITAL SYSTEMS ECT 678 SMART AND PHASED ARRAY ANTENNA DESIGN ECT 40/ECT 616 COMUPTER ARITHMETIC & MICROARCHITECTURE DESIGN ECT 686 PHOTONIC INTEGRATED DEVICES AND SYSTEMS ECT 409/ECT 622 SYSTEM LEVEL DESIGN & MODELLING ECT 680 DWIANCED TOPICS IN COMMUNICATION ECT 409/ECT 623 SYSTEM LEVEL DESIGN & MICROARCONTROLLERS ECT 680 PHOTONIC INTEGRATED DEVICES AND SYSTEMS ECT 411 ADVANCED INFORMORY ECT 680 WIRELESS SENSOR NETWORK ECT 692 ECT 412 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 692 COMPUTATIONAL ELECTRONICG SAND CANDINENTS ECT 451/ECT 685 ADV.ANCED ANTENNA SYSTEMS ECT 692 ADVANCED NETWORKING ANALYSIS ECT 452 COMPUTER RETERTANT PANTENNA ECT 693 ADVANCED NETWORKING ANALYSIS ECT 453 ADVANCED ANTENNA SYSTEMS ECT 692 ADVANCED NETWORKING ANALYSIS ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 693 ADVANCED CONPUTER ARTHMETIC LINE | ECT 401 | SPREAD SPECTRUM TECHNOLOGY | ECT 672 | WIRELESS AND MOBILE ADHOC NETWORKING |
| ECT 405 IMAGE PROCESSING ECT 406/ECT607 CAD ALGORITHMS FOR VLSI PHYSICAL DESIGN ECT 406/ECT607 CAD ALGORITHMS FOR SYLTHESIS OF DIGITAL SYSTEMS ECT 407/ECT 603 CAD ALGORITHMS FOR SYNTHESIS OF DIGITAL SYSTEMS ECT 407/ECT 603 CAD ALGORITHMS FOR SYNTHESIS OF DIGITAL SYSTEMS ECT 409/ECT622 SYSTEM LEVEL DESIGN & MODELLING ECT 409/ECT622 SYSTEM LEVEL DESIGN & MODELLING ECT 411 NEURAL NETWORKS ECT 412 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 413 COMPUTER NETWORKS ECT 414 COMPUTER NETWORKS ECT 451/ECT665 ADV. MICROBARCED ANTENNA SYSTEMS ECT 452 COMPUTER NETWORKS ECT 451 COMPUTER NETWORKS ECT 452 COMPUTER NETWORKS ECT 453 ADVANCED ANTENNA SYSTEMS ECT 454 MICROBARCED ANTENNA SYSTEMS ECT 455 POWER ELECTRONICS ECT 456 DOWANCED DIGTTAL SIGNAL MAGE PROCESSING | CT 403/ECT663 | Advanced ERROR CONTROL CODES | ECT 674 | CRYPTOGRAPHY |
| ECT 406/ECT607 CAD ALGORITHMS FOR VLSI PHYSICAL DESIGN ECT 607 CAD ALGORITHMS FOR VLSI PHYSICAL DESIGN ECT 407/ECT 603 CAD ALGORITHMS FOR SYNTHESIS OF DIGITAL SYSTEMS ECT 684 ADVANCED TOPICS IN COMMUNICATION ECT 409/ECT 616 COMUPTER ARITHMETIC & MICROARCHITECTURE DESIGN ECT 684 ADVANCED TOPICS IN COMMUNICATION ECT 409/ECT 622 SYSTEM LEVEL DESIGN & MODELLING ECT 681 ECT 682 ECT 684 ECT 411 NEURAL. NETWORKS ECT 682 ECT 690 WIRLESS SENSOR NETWORK ECT 451 COMPUTER NETWORKS ECT 694 ADVANCED PHOTONIC DEVICES AND COMPONENTS ECT 451/ECT665 ADV. MICROWAVE ENGG ECT 696 TELECOMMUNICATION TECHNOLOGY AND MANAGEMENT ECT 452 COMPUTER NETWORKS ECT 696 TELECOMMUNICATION TECHNOLOGY AND MANAGEMENT ECT 452 Dasign of MICROSTRIP ANTENNA ECT 697 ADVANCED INFENNA SYSTEMS ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 620 MICRO AND NANO ELECTRO MICRA AND ASIST ECT 4545 POWER ELECTRONICS ECT 633 ADVANCED DIGITAL SIGNAL & MAGE PROCESSING ECT 457/ECT 628 MEMORY DESIGN & TESTING ECT 634 MICRO AND NANO ELECTRO MICRA AND ASIST ECT 4597 MEMORY | CT404/ECT 670 | SATELLITE COMMUNICATION & RADAR ENGINEERING | ECT 678 | DESIGN OF MIC AND MMIC'S |
| ECT 407/ECT 603 CAD ALGORTTHMS FOR SYNTHESIS OF DIGITAL SYSTEMS ECT 684 ADVANCED TOPICS IN COMMUNICATION ECT 408/ECT 616 COMUPTER ARITHMETIC & MICROARCHITECTURE DESIGN ECT 686 PHOTONIC INTEGRATED DEVICES AND SYSTEMS ECT 409/ECT622 SYSTEM LEVEL DESIGN & MODELLING ECT 687 ECT 688 EMIEMC ECT 411 NEURAL NETWORKS ECT 690 WIRELESS SENSOR NETWORK ECT 412 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 692 COMPUTER NETWORKS ECT 451 COMPUTER NETWORKS ECT 694 ADVANCED PHOTONIC DEVICES AND COMPONENTS ECT 451/ECT6665 ADV. MICROWAVE ENGG ECT 694 ADVANCED NETWORKING ANALYSIS ECT 452/ECT676 Design of MICROSTRIP ANTENNA ECT 694 ADVANCED NETWORKING ANALYSIS ECT 453 ADVANCED ANTENNA SYSTEMS ECT 692 ADVANCED NETWORKING ANALYSIS ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 692 ADVANCED NETWORKING ANALYSIS ECT 454 MOROWAVE INTEGRATED CIRCUITS ECT 692 ADVANCED NETWORKING ANALYSIS ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 692 ADVANCED NETWORKING ANALYSIS ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 693 AD | ECT 405 | IMAGE PROCESSING | ECT 680 | ADVANCED MOBILE SYSTEMS |
| ECT 408/ECT 616 COMUPTER ARITHMETIC & MICROARCHITECTURE DESIGN ECT 409/ECT622 SYSTEM LEVEL DESIGN & MODELLING ECT 409/ECT622 SYSTEM LEVEL DESIGN & MODELLING ECT 411 NEURAL NETWORKS ECT 412 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 413 COMPUTER NETWORKS ECT 451/ECT665 ADV. MICROWAVE ENGG ECT 451/ECT666 ADV. MICROWAVE ENGG ECT 452/ECT676 Design of MICROSTRIP ANTENNA ECT 453 ADVANCED ANTENNA SYSTEMS ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 455 POWER ELECTRONIC DIC DEVICES AND CIRCUIT ECT 456 SEMICONDUCTOR OPTO-ELECTRONICS ECT 457 POWER ELECTRONICS ECT 458 SEMICONDUCTOR OPTO-ELECTRONICS ECT 459/ECT640 ELECTRONIC MANUFACTURING TECHNOLOGY ECT 459/ECT640 ELECTRONIC MANUFACTURING TECHNOLOGY ECT 459/ECT640 ELECTRONIC MANUFACTURING TECHNOLOGY ECT 462 FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED Software ECT 462 FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED Software ECT 462 FORMAL VERIFICATION OF DIGINE ERING ECT 462 PARALLEL COMPUTING ARCH | CT 406/ECT607 | CAD ALGORITHMS FOR VLSI PHYSICAL DESIGN | ECT 682 | SMART AND PHASED ARRAY ANTENNA DESIGN |
| ECT 409/ECT622 SYSTEM LEVEL DESIGN & MODELLING ECT 688 EM//EMC ECT 411 NEURAL NETWORKS ECT 690 WIRELESS SENSOR NETWORK ECT 412 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 692 COMPUTATIONAL ELECTROMAGNETIC ECT 413 COMPUTER NETWORKS ECT 694 ADVANCED PHOTONIC DEVICES AND COMPONENTS ECT 451/ECT665 ADV. MICROWAVE ENGG ECT 696 TELECOMMUNICATION TECHNOLOGY AND MANAGEMENT ECT 452 ADVANCED ANTENNA SYSTEMS ECT 696 TELECOMMUNICATION TECHNOLOGY AND MANAGEMENT ECT 453 ADVANCED ANTENNA SYSTEMS ECT 620 ADVANCED DIGITAL SIGNAL & IMAGE PROCESSING ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 630 ADVANCED COMPUTER ARCHITECTURE ECT 455 POWER ELECTRONICS ECT 630 ADVANCE ONE CHECTRONIC DEVICES AND CIRCUITS ECT 457 ESMICONDUCTOR OPTO-ELECTRONICS ECT 634 MICRO AND NANO ELECTRO MECHANICAL SYSTEMS ECT 459/ECT640 ELECTRONIC MANUFACTURING TECHNOLOGY ECT 650 SPECIAL TOPICS IN VLSI-1 ECT 462 APAILIEL COMPUTING ARCH ECT 652 SPECIAL TOPICS IN VLSI-1 ECT 464 BIO-MEDICAL ENGINEERING ECT 654 RF INTEGRATED CIRCUITS | T 407/ECT 603 | CAD ALGORITHMS FOR SYNTHESIS OF DIGITAL SYSTEMS | ECT 684 | ADVANCED TOPICS IN COMMUNICATION |
| ECT 411 NEURAL NETWORKS ECT 411 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 412 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 413 COMPUTER NETWORKS ECT 413 COMPUTER NETWORKS ECT 451/ECT665 ADV. MICROWAVE ENGG ECT 452/ECT676 Design of MICROSTRIP ANTENNA ECT 453 ADVANCED ANTENNA SYSTEMS ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 455 POWER ELECTRONICS ECT 456 SEMICONDUCTOR OPTO-ELECTRONICS ECT 456 SEMICONDUCTOR OPTO-ELECTRONICS ECT 456 MICRO AND PARCHURING TECHNOLOGY ECT 456 MEMORY DESIGN & TESTING ECT 463 MEMORY DESIGN & TESTING ECT 463 DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITS ECT 456 FORMAL VERIFICATION OF DIGITAL HARDWARE & EMBEDDED Software ECT 624 ADVANCED CONPUTER ARCHITECTION ECT 463 PARALLEL COMPUTING ARCH ECT 464 BIO-MEDICAL ENGINEERING ECT 4650 OPTICA | T 408/ECT 616 | COMUPTER ARITHMETIC & MICROARCHITECTURE DESIGN | ECT 686 | PHOTONIC INTEGRATED DEVICES AND SYSTEMS |
| ECT 412 ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS ECT 692 COMPUTATIONAL ELECTROMAGNETIC ECT 413 COMPUTER NETWORKS ECT 694 ADVANCED PHOTONIC DEVICES AND COMPONENTS ECT 451/ECT665 ADV. MICROWAVE ENGG ECT 694 ADVANCED PHOTONIC DEVICES AND COMPONENTS ECT 452/ECT676 Design of MICROSTRIP ANTENNA ECT 696 TELECOMMUNICATION TECHNOLOGY AND MANAGEMENT ECT 453 ADVANCED ANTENNA SYSTEMS ECT 692 ADVANCED DIGITAL SIGNAL & IMAGE PROCESSING ECT 454 MICROWAVE INTEGRATED CIRCUITS ECT 620 MICROELECTRONIC DEVICES AND CIRCUIT ECT 455 POWER ELECTRONICS ECT 630 ADVANCED COMPUTER ARCHITECTURE ECT 456 SEMICONDUCTOR OPTO-ELECTRONICS ECT 634 MICRO AND NANO ELECTO MECHANICAL SYSTEMS ECT 456 SEMICONDUCTOR OPTO-ELECTRONICS ECT 634 MICRO AND NANO ELECTON MECHANICAL SYSTEMS ECT 456 SEMICONDUCTOR OPTO-ELECTRONICS ECT 664 ESTIMATION AND DETECTION ECT 462 FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED Software ECT 650 SPECIAL TOPICS IN VLSI-1 ECT 462 FORMAL VERIFICATION OF DIgital HARDWARE & EMBEDDED Software ECT 652 SPECIAL TOPICS IN VLSI-2 ECT 463 PARALLEL COMPUTI | CT 409/ECT622 | SYSTEM LEVEL DESIGN & MODELLING | ECT 688 | EMI/EMC |
| ECT 413COMPUTER NETWORKSECT 694ADVANCED PHOTONIC DEVICES AND COMPONENTSECT 451/ECT665ADV. MICROWAVE ENGGECT 696TELECOMMUNICATION TECHNOLOGY AND MANAGEMENTECT 452/ECT676Design of MICROSTRIP ANTENNAECT 698ADVANCED NETWORKING ANALYSISECT 453ADVANCED ANTENNA SYSTEMSECT 698ADVANCED DIGITAL SIGNAL & IMAGE PROCESSINGECT 454MICROWAVE INTEGRATED CIRCUITSECT 620MICROELECECT 455POWER ELECTRONICSECT 630ADVANCED COMPUTER ARCHITECTUREECT 456SEMICONDUCTOR OPTO-ELECTRONICSECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 457/ECT 628MEMORY DESIGN & TESTINGECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 654RF INTEGRATED CIRCUITSECT 466/ECT 655CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 466/ECT 655ADAPTIVE SIGNAL PROCESSINGECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | ECT 411 | NEURAL NETWORKS | ECT 690 | WIRELESS SENSOR NETWORK |
| ECT 451/ECT665ADV. MICROWAVE ENGGECT 696TELECOMMUNICATION TECHNOLOGY AND MANAGEMENTECT 452/ECT676Design of MICROSTRIP ANTENNAECT 698ADVANCEDNETWORKING ANALYSISECT 453ADVANCED ANTENNA SYSTEMSECT 662ADVANCED DIGITAL SIGNAL & IMAGE PROCESSINGECT 454MICROWAVE INTEGRATED CIRCUITSECT 620MICROELECTRONIC DEVICES AND CIRCUITECT 455POWER ELECTRONICSECT 630ADVANCED COMPUTER ARCHITECTUREECT 456SEMICONDUCTOR OPTO-ELECTRONICSECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 457/ECT 628MEMORY DESIGN & TESTINGECT 638DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITSECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 654RF INTEGRATED CIRCUITSECT 465/ECT658CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/FECT 656ADAPTIVE SIG NAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/FECT 655OPTICAL CODES AND APPLICATIONSECT 465/FECT 655ADAPTIVE SIG | ECT 412 | ADVANCED MICROPROCESSORS & MICRO-CONTROLLERS | ECT 692 | COMPUTATIONAL ELECTROMAGNETIC |
| ECT 452/ECT676Design of MICROSTRIP ANTENNAECT 698ADVANCED NETWORKING ANALYSISECT 453ADVANCED ANTENNA SYSTEMSECT 662ADVANCED DIGITAL SIGNAL & IMAGE PROCESSINGECT 454MICROWAVE INTEGRATED CIRCUITSECT 620MICROELECTRONIC DEVICES AND CIRCUITECT 455POWER ELECTRONICSECT 630ADVANCED COMPUTER ARCHITECTUREECT 456SEMICONDUCTOR OPTO-ELECTRONICSECT 630MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 457/ECT 628MEMORY DESIGN & TESTINGECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 465/ECT 655OPTICAL COMPUTING ARCHECT 991ECT 465/ECT 655OPTICAL CODES AND APPLICATIONSECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 467/ECT 656ADAPTIVE SIGNAL PROCESSING | ECT 413 | COMPUTER NETWORKS | ECT 694 | ADVANCED PHOTONIC DEVICES AND COMPONENTS |
| ECT 453ADVANCED ANTENNA SYSTEMSECT 662ADVANCED DIGITAL SIGNAL & IMAGE PROCESSINGECT 454MICROWAVE INTEGRATED CIRCUITSECT 620MICROELECTRONIC DEVICES AND CIRCUITECT 455POWER ELECTRONICSECT 630ADVANCED COMPUTER ARCHITECTUREECT 456SEMICONDUCTOR OPTO-ELECTRONICSECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 457/ECT 628MEMORY DESIGN & TESTINGECT 634MICRO AND NANO ELECTRONOUS SEQUENTIAL CIRCUITSECT 459/ECT 640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 654RF INTEGRATED CIRCUITSECT 465/ECT658CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 991MATHEMATICAL METHODS & TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 466/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNOLOGI | CT 451/ECT665 | ADV. MICROWAVE ENGG | ECT 696 | TELECOMMUNICATION TECHNOLOGY AND MANAGEMENT |
| ECT 454MICROWAVE INTEGRATED CIRCUITSECT 620MICROELECTRONIC DEVICES AND CIRCUITECT 455POWER ELECTRONICSECT 630ADVANCED COMPUTER ARCHITECTUREECT 456SEMICONDUCTOR OPTO-ELECTRONICSECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 457/ECT 628MEMORY DESIGN & TESTINGECT 638DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITSECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 638DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITSECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT658CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 656ADAPTIVE SIGNAL PROCESSINGECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | CT 452/ECT676 | Design of MICROSTRIP ANTENNA | ECT 698 | ADVANCED NETWORKING ANALYSIS |
| ECT 455POWER ELECTRONICSECT 630ADVANCED COMPUTER ARCHITECTUREECT 456SEMICONDUCTOR OPTO-ELECTRONICSECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 457/ECT 628MEMORY DESIGN & TESTINGECT 634DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITSECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 664ESTIMATION AND DETECTIONECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 650SPECIAL TOPICS IN VLSI-1ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT658CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992 | ECT 453 | ADVANCED ANTENNA SYSTEMS | ECT 662 | ADVANCED DIGITAL SIGNAL & IMAGE PROCESSING |
| ECT 456SEMICONDUCTOR OPTO-ELECTRONICSECT 634MICRO AND NANO ELECTRO MECHANICAL SYSTEMSECT 457/ECT 628MEMORY DESIGN & TESTINGECT 634DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITSECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | ECT 454 | MICROWAVE INTEGRATED CIRCUITS | ECT 620 | MICROELECTRONIC DEVICES AND CIRCUIT |
| ECT 457/ECT 628MEMORY DESIGN & TESTINGECT 638DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITSECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | ECT 455 | POWER ELECTRONICS | ECT 630 | ADVANCED COMPUTER ARCHITECTURE |
| ECT 459/ECT640ELECTRONIC MANUFACTURING TECHNOLOGYECT 664ESTIMATION AND DETECTIONECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT 655CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | ECT 456 | SEMICONDUCTOR OPTO-ELECTRONICS | ECT 634 | MICRO AND NANO ELECTRO MECHANICAL SYSTEMS |
| ECT 460/ECT 626FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED SoftwareECT 650SPECIAL TOPICS IN VLSI-1ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT 655CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | T 457/ECT 628 | MEMORY DESIGN & TESTING | ECT 638 | DESIGN OF ASYNCHRONOUS SEQUENTIAL CIRCUITS |
| ECT 462ARTIFICIAL INTELLIGENCE & EXPERT SYSTEMECT 652SPECIAL TOPICS IN VLSI-2ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRATED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT 655CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | CT 459/ECT640 | ELECTRONIC MANUFACTURING TECHNOLOGY | ECT 664 | ESTIMATION AND DETECTION |
| ECT 463PARALLEL COMPUTING ARCHECT 654RF INTEGRA TED CIRCUITSECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT658CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | T 460/ECT 626 | FORMAL VERIFICATION OF Digital HARDWARE & EMBEDDED Software | ECT 650 | SPECIAL TOPICS IN VLSI-1 |
| ECT 464BIO-MEDICAL ENGINEERINGECT 991MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 465/ECT 658CURRENT-MODE ANALOG SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 466/ECT 655OPTICAL CODES AND APPLICATIONSECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIECT 467/ECT 656ADAPTIVE SIGNAL PROCESSINGECT 992MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGI | ECT 462 | ARTIFICIAL INTELLIGENCE & EXPERT SYSTEM | ECT 652 | SPECIAL TOPICS IN VLSI-2 |
| ECT 465/ECT658 CURRENT-MODE ANALOG SIGNAL PROCESSING ECT 992 MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGII ECT 466/ECT 655 OPTICAL CODES AND APPLICATIONS ECT 467/ECT 656 ADAPTIVE SIGNAL PROCESSING | ECT 463 | PARALLEL COMPUTING ARCH | ECT 654 | RF INTEGRATED CIRCUITS |
| ECT 466/ECT 655 OPTICAL CODES AND APPLICATIONS ECT 467/ECT 656 ADAPTIVE SIGNAL PROCESSING | ECT 464 | BIO-MEDICAL ENGINEERING | ECT 991 | MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIES I |
| ECT 467/ECT 656 ADAPTIVE SIGNAL PROCESSING | CT 465/ECT658 | CURRENT-MODE ANALOG SIGNAL PROCESSING | ECT 992 | MATHEMATICAL METHODS & TECHNIQUES FOR ECE TECHNOLOGIES II |
| | T 466/ECT 655 | OPTICAL CODES AND APPLICATIONS | | |
| ECT 468/ECT 657 VLSI SIGNAL PROCESSING Architectures | T 467/ECT 656 | ADAPTIVE SIGNAL PROCESSING | | |
| | T 468/ECT 657 | VLSI SIGNAL PROCESSING Architectures | | |
| ECT470 Human Values -I | ECT470 | Human Values -I | | |
| ECT478/ECT 642 FPGA PHYSICAL DESIGN | CT478/ECT 642 | FPGA PHYSICAL DESIGN | | |
| ECT479/ECT 614 VLSI TECHNOLOGY | CT479/ECT 614 | VLSI TECHNOLOGY | | |
| ECT480 Information Theory & Coding | ECT480 | Information Theory & Coding | | |
| ECT481 System Design using FPGAs | ECT481 | System Design using FPGAs | — | |
| ECT482 Instrumentation & Control | ECT482 | Instrumentation & Control | | |

(Department of Electronics & Communication Engineering)

| Example-1 | Semester VII | | | Student goes on INTERNSHIP in 7th Sem | SUM should be in | the range 18- | -20 | | |
|-----------|--------------|----------|-------------|---------------------------------------|------------------|---------------|--------|-------|--|
| | S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-P | |
| | 1 | VII | | Management* | PC | Theory | 3 | 3-0-0 | a |
| | 5 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0-0 | ed as manua lits |
| | 6 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0-0 | Exempted per UG ma (16 credits max) |
| | 7 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0-0 | |
| | 2 | VII | ECD 483 | System Design Lab-I | PC | Lab | 2 | 0-0-3 | Exempted a per UG ma (16 credits max) |
| | 1 | VII | ECD 481 | Training Seminar | PC | Lab | 2 | 0-0-3 | in 8th Sem |
| | 3 | VII | ECD 498 | Major Project A | PC | Project | 4 | 0-0-8 | in 8th Sem |
| | | | | | | | 0 | | |

Semester VIII SUM should be in the range 18-20 S.No. Semester Course Code Course Name Category Туре Credit L-T-P 1 VIII FROM LIST OE Theory 3-0-0 2 3 VIII FROM LIST OE 3 3-0-0 3 Theory VIII FROM AEC LIST AEC 3-0-0 4 Theory 3 FROM AEC LIST AEC 3-0-0 5 VIII Theory 3 VIII FROM AEC LIST AEC 3-0-0 6 3 Theory VIII FROM AEC LIST 3 3-0-0 7

| 1 | VIII | ECD 482 | System Design Lab-II | PC | Lab | 2 | 0-0-3 |
|---|------|---------|--|----|---------|----|--------|
| 2 | VIII | ECD 499 | Major Project B+ evaluation ONLY for A | PC | Project | 8 | 0-0-16 |
| | | | | | | 00 | |

20

(Department of Electronics & Communication Engineering)

Example-2

| ester VII | | | SUM should be in the range 18-20 | | | | |
|-----------|----------|-------------|----------------------------------|----------|---------|--------|-------|
| S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-P |
| 1 | VII | | Management | PC | Theory | 3 | 3-0-0 |
| 4 | VIII | | FROM LIST | OE | Theory | 3 | 3-0-0 |
| | VIII | | FROM LIST | OE | Theory | 3 | 3-0-0 |
| 5 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0-0 |
| 6 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0-0 |
| 7 | VII | | FROM PE LIST | PE | Theory | 3 | 3-0-0 |
| 2 | VII | ECD 483 | System Design Lab-I | PC | Lab | 2 | 0-0-3 |
| 1 | VII | ECD 481 | Training Seminar | PC | - | 2 | 0-0-3 |
| 3 | VII | ECD 498 | Major Project A | PC | Project | 4 | 0-0-8 |
| | | | | | | | |
| | | | | | | 20 | |

| S.No. | Semester | Course Code | Course Name | Category | Туре | Credit | L-T-P | |
|-------|----------|-------------|-----------------------------------|----------|---------|--------|--------|-------------------|
| 3 | VIII | | FROM AEC LIST | OE | Theory | 3 | 3-0-0 | ø |
| 5 | VIII | | FROM AEC LIST | AEC | Theory | 3 | 3-0-0 | as anu |
| 6 | VIII | | FROM AEC LIST | AEC | Theory | 3 | 3-0-0 | JG mé |
| 7 | VIII | | FROM AEC LIST | AEC | Theory | 3 | 3-0-0 | |
| 8 | VIII | ECD 482 | System Design Lab-II | PC | Lab | 2 | 0-0-3 | Exe per (16 |
| 9 | VIII | ECD 499 | Major Project B (evaluation ONLY) | PC | Project | 8 | 0-0-16 | End of 8th S |

*Indicates that the courses individually, may be opted either in 7th Semester OR 8th Semester