Electronics & ICT Academy

Malaviya National Institute of Technology Jaipur

16th Oct. 2017

<u>Subject:</u> Call for interview for the post of **Program Manager** to beheld on 06th November 2017 (Monday), at 9 AM

Dear Candidate,

Consequent to your application for the post of Program Manager, you are hereby informed that test/interview for this post is scheduled on 06th November 2017, starting 9 AM at 1st Floor Guest House-1 (Indradhanush). You should come prepared to stay for an extra day on 07th November 2017 (if required). Please see list (I) of Annexure-A attached, eligible candidates.

Contact Mr. Suraj Soni at Cellphone- 954 965 9321 or through emailsonisuraj80@gmail.com for logistic requirements. The institute will provide free accommodation for the night of 6th Nov., for which you have to confirm your participation by 27th Nov. (Monday).

Important information for candidates-

- 1. No TA/DA will be paid for attending the written test / interview.
- 2. Candidates will have to report for written test/interview with their detailed bio-data, 02 passport size photos, original and attested photocopies of all certificates, mark sheets, degree and testimonials. They must also bring any Govt. issued ID- passport, Aadhaar, PAN or Voter ID in original with one spare copy.
- 3. Persons employed in Government/Semi- Government Organization or Educational
- 4. Institutions are required to produce a no-objection certificate at the time of interview (if shortlisted for interview).
- 5. No correspondence will be entertained from candidates regarding conduct & result of written test / interview, reasons for not being called for interview etc.
- 6. Canvassing in any form will be a disqualification.
- 7. Applicants are requested to keep visiting the website of MNIT Jaipur or the Electronics and ICT Academy (http://mnit.ac.in/eict/) for latest and updated information etc. in particular this link- http://mnit.ac.in/eict/recruitment.php
- 8. The Institute reserves the right to fill or not to fill any of the posts advertised.
- 9. For test, the syllabus for reading may cover basic subjects from curriculum of B.Tech./M.Tech. in Communication, VLSI and Computer Engineering- viz. Digital & Analog communication, CMOS digital & Analog Circuits, Advanced Data structures and algorithms, Digital systems design & FPGAs, Advanced Computer Architecture, DSP etc.

Best Wishes

(Prof. Vineet Sahula)

Chief Investigator, EICT Academy MNIT Jaipur